

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: June 20, 2007

Augustine W. Chang

Confirmation No. 2174

Serial No: 10/817,201

Group Art Unit: 2815

Filed: 04/02/2004

Examiner: Budd, Paul A.

For: Variable Threshold Transistor for the Schottky FPGA and Multilevel Storage Cell
Flash Arrays

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated February 14, 2007, please amend the above-identified application in the following manner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.